

KCO-800P Series

Programmable Ceramic SMD Crystal Clock Oscillators

Frequency range	1.0 ~ 200.0 MHz		1 ~ 80.0 MHz
Frequency stability	± 100ppm, ± 50ppm		
Operating temperature range	-10 ~ +70		
Input Voltage	3.3 ± 0.3V, 3.0 ± 0.3V, 5.0 ± 0.5V		
Input Current	3.3V ± 0.3V	30mA Max.	20mA Max.
	5.0V ± 0.5V	50mA Max.	30mA Max.
Output Load	15pF CMOS		50pF CMOS
Duty Cycle	40:60		
Jitter (p-p)	100ps, 200ps, 250ps Max.		
Start Up Time	10ms Max.		
Programmable Parameter			
Input Voltage	3.0 ± 0.3V		
	3.3 ± 0.3V		
	5.0 ± 0.5V		
Output Load	15pF (1~200 MHz)		
	50pF (1~80 MHz)		
Frequency Stability	± 100ppm		
	± 50ppm		
	± 30ppm(option)		
Control Function #1 Low	Output Hi-z	Output Stop/PLL run	
	Output Hi-z	Oscillation Stop	

All specs are subject to change without notice.

